

## Description

# ALTERNATING PHASE MASK BUILT BY ADDITIVE FILM DEPOSITION

### BACKGROUND OF INVENTION

[0001] The present invention generally relates to phase shift masks and more particularly to an improved phase shift mask that uses liquid phase deposition oxide as a phase shift material.

[0002] As optical lithography is extended to images substantially smaller than the exposure wavelength, the use of resolution enhancement techniques is needed, such as altering (alternating) phase shift. The fabrication of altering phase shift masks is limited by the uniformity capabilities of the etch processes that define the phase shift regions. The etch processes do not have an etch stop layer, so the inherent pattern factor and across substrate variations in the RIE chamber are replicated in the substrate. This variation in etch depth causes a small focus window and an effectively reduced resolution. The invention described

below addresses these issues.

## **SUMMARY OF INVENTION**

[0003] The invention provides a method of forming a phase shift mask and (duplicate) phase shift mask. The method forms a non-transparent (e.g., absorptive, reflective) film on a transparent substrate and patterns an etch stop layer on the non-transparent film. The invention patterns the non-transparent film using the etch stop layer to expose areas of the transparent substrate. Next, the invention forms a mask on the non-transparent film to protect selected areas of the transparent substrate and forms a phase shift oxide on exposed areas of the transparent substrate. Subsequently, the mask is removed and the phase shift oxide is polished down to the etch stop layer, after which the etch stop layer is removed. More specifically, the phase shift oxide is formed by placing the transparent substrate and the non-transparent film within a deposition bath.

[0004] The polishing process and the thickness of the etch stop layer control the thickness of the phase shift oxide. Further, the process of forming the phase shift oxide is controlled to adjust the optical properties of the phase shift oxide.

[0005] The process of patterning the etch stop layer comprises

forming an organic base layer on the non-transparent film, forming a nitride etch stop layer on the base layer, and forming a photoresist on the nitride etch stop layer. The invention exposes and develops the photoresist and then etches the nitride etch stop layer and the organic base layer through the resist layer. Openings in the mask are formed above openings within the non-transparent film to allow selected areas of the transparent substrate (where the phase shift oxide is to be formed) to be exposed.

[0006] In an alternative embodiment, the invention avoids the need to perform the polishing process by patterning a non-transparent film on a transparent substrate to expose areas of the transparent substrate, forming a mask on the non-transparent film to protect selected areas of the transparent substrate, forming a phase shift oxide on exposed areas of the transparent substrate, and removing the mask.

[0007] The foregoing processes produce a phase shift mask that comprises a transparent substrate, a patterned non-transparent film above the transparent substrate, and a liquid phase deposition oxide phase shift material within selected openings of the patterned non-transparent film.

[0008] The invention provides a number of advantages when compared to conventional structures and methods. For example, the invention utilizes an etch stop layer which eliminates the variations in the thicknesses of the phase shifters. Similarly, in the second embodiment, the thickness of the phase shifter is uniform across the wafer because of the liquid phase deposition process grows the oxide to uniform thicknesses. In addition, by utilizing the mask that includes the etch stop layer, a separate etch stop layer does not need to be formed.

[0009] These, and other, aspects and objects of the present invention will be better appreciated and understood when considered in conjunction with the following description and the accompanying drawings. It should be understood, however, that the following description, while indicating preferred embodiments of the present invention and numerous specific details thereof, is given by way of illustration and not of limitation. Many changes and modifications may be made within the scope of the present invention without departing from the spirit thereof, and the invention includes all such modifications.

#### **BRIEF DESCRIPTION OF DRAWINGS**

[0010] The invention will be better understood from the following

detailed description with reference to the drawings, in which:

- [0011] Figure 1 is a schematic diagram of a partially completed phase shift mask;
- [0012] Figure 2 is a schematic diagram of a partially completed phase shift mask;
- [0013] Figure 3 is a schematic diagram of a partially completed phase shift mask;
- [0014] Figure 4 is a schematic diagram of a partially completed phase shift mask;
- [0015] Figure 5 is a schematic diagram of a partially completed phase shift mask;
- [0016] Figure 6 is a schematic diagram of a partially completed phase shift mask;
- [0017] Figure 7 is a schematic diagram of a partially completed phase shift mask;
- [0018] Figure 8 is a schematic diagram of a partially completed phase shift mask;
- [0019] Figure 9 is a schematic diagram of a partially completed phase shift mask; and
- [0020] Figure 10 is a flow diagram illustrating a preferred method of the invention.

## **DETAILED DESCRIPTION**

[0021] The present invention and the various features and advantageous details thereof are explained more fully with reference to the nonlimiting embodiments that are illustrated in the accompanying drawings and detailed in the following description. It should be noted that the features illustrated in the drawings are not necessarily drawn to scale. Descriptions of well-known components and processing techniques are omitted so as to not unnecessarily obscure the present invention. The examples used herein are intended merely to facilitate an understanding of ways in which the invention may be practiced and to further enable those of skill in the art to practice the invention. Accordingly, the examples should not be construed as limiting the scope of the invention.

[0022] The invention uses an additive film deposition (e.g., LPD) approach to forming the phase shifter, avoiding the problems of etch loading and pattern density. More specifically, the invention uses Liquid Phase Deposition (LPD) of oxide to form the phase shifters on the mask substrate. One embodiment of the invention is shown in Figures 1–6 and a second embodiment is shown in Figures 7–9.

[0023] As shown in Figure 1, the invention begins with a standard mask blank with a reflective mask layer 12 (e.g.,

about 100–200 nm of chrome, tungsten, molybdenum, molybdenum silicide, etc.) over a transparent or semi-transparent substrate 10 (e.g., quartz, fluorinated quartz,  $\text{CaF}_2$ , hafnium oxide, etc.). Item 14 illustrates a resist film that is formed over the mask layer 12. The resist film 14 could comprise any appropriate resist. For example, the resist film 14 could comprise a tri-layer resist, with a hard-baked organic underlayer 16, silicon nitride polish stop layer 17, and E beam resist layer 18. The thickness of the hard baked underlayer 16 and the nitride polish stop 17 should be equal to the desired thickness of the phase shift material that is formed later.

[0024] As shown in Figure 2, a mask pattern is exposed in the E beam resist 18, which is then developed. Next, the nitride polish stop 17 and hard baked organic layer 16 are etched in, for example, an oxygen reactive ion etching (RIE) process. This patterning and etching process creates patterned openings 20 in the resist film 14.

[0025] As shown in Figure 3, the chrome 12 is then etched using the resist 14 as a mask to create openings 30. After the chrome etch, the initial E beam resist layer 17 is removed, leaving the nitride polish stop 17 as the surface film of the resist 14.

[0026] Then, as shown in Figure 4, another patterned mask 40 is formed to protect openings 30 in which the phase shift material is to not be formed. Mask 40 is an alternating mask, where every other opening 30 is protected, (usually, but not always, it may depend on the distance between adjacent features and the specific pattern layout of an array of semiconductor devices).

[0027] Next, a liquid-phase silicon dioxide 42 is grown onto the exposed quartz surface 10 through the unprotected ones of openings 30 by placing the structure in a deposition bath. The LPD oxide 42 will selectively grow on the quartz, not on the resist 40 or nitride hard mask 17.

[0028] More specifically, the patterned mask shown in Figure 4 is immersed in an aqueous solution of  $\text{SiF}_6$ , which has reached supersaturation, either by warming from 0C to 15–35C, or by adding  $\text{H}_2\text{BO}_3$ , or by adding chips of aluminum metal. The boric acid and aluminum function by withdrawing fluorine from the system, causing supersaturation of  $\text{SiF}_6$ , and pushing the chemical equilibrium towards the formation silicon oxide. The silicon oxide deposits selectively on the exposed quartz surface of the mask, not on the resist or silicon nitride. The oxide growth occurs typically at 10–40 nm per minute. The de-



position is continued until a small excess, 2–10%, of oxide is formed relative to the Cr/silicon nitride mask thickness. The refractive index of the LPD oxide 42 can be matched to the mask blank, if desired, by controlling the growth temperature of the liquid solution. The refractive index can be varied between 1.433 and 1.423 as the deposition temperature increases from 15 C to 35 C. The lower refractive index is usually associated with slightly less dense oxide. At 35 C they had deposition rates of about 100 nm/hour, as opposed to deposition rates of about 10 nm/hour at 15C. There is a slightly higher Fluorine content with the higher temperature deposition conditions, and also more hydroxyl functionality at the higher deposition temperatures (J. Electrochem, Soc. Vol. 147, pp.330–334, 2000).

[0029] After the structure is removed from the deposition bath, the mask 40 is removed, as shown in Figure 5. In Figure 6, a chemical–mechanical polishing process is used to trim back the film thickness of the LPD oxide 42 to the nitride polish stop surface 17. Thus, the thickness of the LPD oxide 12 is determined by the initial organic layer/nitride etch stop 16, 17 film stack thickness, less any nitride thickness consumed in the CMP process. In one example,

the shifter thickness is usually about 150nm for 193nm lithography.

[0030] Thus, the process shown above uses CMP to attain a uniform thickness phase shifter. In this process the invention uses a selective deposition of silicon oxide on a patterned mask substrate in order to additively deposit a uniform layer of phase shifter material (fluorinated silicon oxide) in the desired regions of the lithography mask. A chemical mechanical polish step (CMP) is used to provide some enhancement to the shifter thickness uniformity, as well as provide a flat optical surface to the shifter. The alternating PSM mask is now complete, and ready for cleaning and pellicle mounting. There are no substantial differences of the LPD oxide verses conventional phase shifters, as they are both silicon oxide in nature.

[0031] In the second embodiment (shown in Figures 7–9) the CMP process is avoided and the oxide shifter thickness is determined only by the uniformity of the LPD process. This is a surface limited deposition and provides good uniformity across the substrate. The process is also selective, in that it only grows on quartz surfaces, not the resist.

[0032] The process begins with the patterned chrome structure

formed to the point shown in Figure 3. However, instead of the three-layer mask 14 utilized in the first embodiment, any conventional mask (such as a single layer mask) can be utilized to pattern the chrome 12. As shown in Figure 7, the mask 14 is removed and a second mask 70 is deposited and patterned to protect the openings 30 in which the LPD oxide phase shifter is not to be formed. Then, using the same processing discussed above, the structure shown (in) Figure 7 is placed in a deposition bath to form the liquid-phase silicon dioxide 80 within the opening 72, as shown in Figure 8. Next, as shown in Figure 9, the resist 70 is removed. The thickness can be monitored and controlled with an optical endpoint detector, based on laser interferometry.

[0033] The second process is similar to that described above, except the CMP step is eliminated, and the invention relies entirely on the liquid phase deposition of oxide (LPD oxide) to establish the uniformity and thickness of the phase shift layer. Thus, when the oxide has grown to the desired thickness, the mask is removed from the bath, the resist is stripped, and the mask is ready for cleaning and pellicle mounting.

[0034] As shown in the flowchart in Figure 10, the invention pro-

vides a method of forming a phase shift mask. The method forms a non-transparent film on a transparent substrate 300 and patterns an etch stop layer on the non-transparent film 302. The invention patterns the non-transparent film 304 using the etch stop layer to expose areas of the transparent substrate. Next, the invention forms a mask on the non-transparent film 306 to protect selected areas of the transparent substrate and forms a phase shift oxide 308 on exposed areas of the transparent substrate. Subsequently, the mask is removed 310 and the phase shift oxide is polished 312 down to the etch stop layer, after which the etch stop layer is removed 314.

[0035] The invention provides a number of advantages when compared to conventional structures and methods. For example, the invention polishes using an etch stop layer which eliminates the variations in the thicknesses of the phase shifters. Similarly, in the second embodiment, the thickness of the phase shifter is uniform across the wafer because the liquid phase deposition process grows the oxide to a uniform thickness. In addition, by utilizing the mask that includes the etch stop layer, a separate etch stop layer does not need to be formed.

[0036] While the invention has been described in terms of preferred embodiments, those skilled in the art will recognize that the invention can be practiced with modification within the spirit and scope of the appended claims.

[0037] What is claimed is: